UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.   : 7,639,651 B2

APPLICATION NO.   : 10/596403
DATED   : December 29, 2009
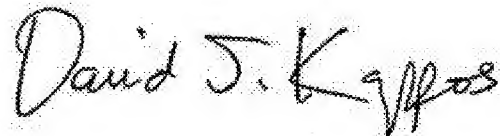INVENTOR(S)   : Di Lodovico et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 11, Lines 10-11, delete "
$$BD_{IP}=(req\_slots*SL*8+(PP-req\_slots)*\lceil \log_2(RTT+N_{RG}-1\rceil)*100/(PP*SL*8).$$
"

and insert --
$$BD_{IP}=(req\_slots*SL*8+(PP-req\_slots)*\lceil \log_2(RTT+N_{RG}-1\rceil)*100/(PP*SL*8).$$
--, therefor.

In Column 12, Line 55, delete " $q_{c,x,i}(t)$ " and insert -- $q_{x,c,i}(t)$ --, therefor.

Signed and Sealed this
Fourth Day of January, 2011

David J. Kappos
Director of the United States Patent and Trademark Office